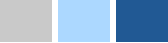


# 10<sup>th</sup> International Conference on Reversible Computation

September 13<sup>th</sup> - 14<sup>th</sup>, 2018, Leicester, UK

# RC2018



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Research in reversible computation has drawn the attention of researchers over the years. Reversible computation has a growing number of promising application areas such as low power design, coding/decoding, testing and verification, database recovery, discrete event simulation, reversible specification formalisms, reversible programming languages, process algebras, quantum computation, etc. First reversible circuits and quantum circuits have been implemented and are seen as promising alternatives to conventional CMOS technology.

The conference will bring together researchers from computer science, mathematics, and physics to discuss new developments and directions for future research in Reversible Computation. This includes applications of reversibility in quantum computation. Research papers, tutorials, tool demonstrations, and work-in-progress reports are within the scope of the conference. Invited talks by leading international experts will complete the program.

**Contributions on all areas of Reversible Computation are welcome, including - but not limited to - the following topics:**

- \* Applications
- \* Architectures
- \* Algorithms
- \* Circuit Design
- \* Debugging
- \* Fault Tolerance and Error Correction
- \* Hardware
- \* Information Theory
- \* Physical Realizations
- \* Programming Languages
- \* Quantum Computation
- \* Software
- \* Synthesis
- \* Theoretical Results
- \* Testing
- \* Verification



Interested researchers are invited to submit full research papers (16 pages maximum), as well as work-in-progress or tool demonstration papers (6 pages maximum) in Springer LNCS format. Contributions must be written in English and report on original, unpublished work, not submitted for publication elsewhere. Additional material intended for reviewers but not for publication in the final version - for example, details of proofs - may be placed in a clearly marked appendix that is not included in the page limit. Reviewers are at liberty to ignore appendices and papers must be understandable without them.

All submitted papers will be peer-reviewed by at least three reviewers for technical merit, originality, significance and relevance to the scope of the conference. All accepted papers will be included in the conference proceedings and published by Springer as an Lecture Notes in Computer Science (LNCS) volume.

## Important Dates:

Abstract Submission: Tue April 10<sup>th</sup>, 2018

Submission Deadline: Tue April 17<sup>th</sup>, 2018

Notification to Authors: Tue May 29<sup>th</sup>, 2018

Final Version: Tue June 12<sup>th</sup>, 2018

Conference: September 13<sup>th</sup> and 14<sup>th</sup>, 2018

## Further information about the conference:

<http://www.reversible-computation.org>

[info@reversible-computation.org](mailto:info@reversible-computation.org)