

9th Conference on Reversible Computation

SUMMER SCHOOL

The Pride Plaza Hotel, Kolkata, India

July 5, 2017

Time	Session
9:30 – 10:15	Reversible Logic: From Foundations to Frontiers <i>Prof. Bhargab Bhattacharyya, Indian Statistical Institute Calcutta, India</i>
10:15 – 11:00	Algebra for Reversible Logic Circuits <i>Prof. M. H. A. Khan, East West University, Dhaka, Bangladesh</i>
11:00 – 11:30	TEA / COFFEE BREAK
11:30 – 12:15	Adiabatic Circuits <i>Dr. Michael P. Frank, Sandia National Laboratories, USA</i>
12:15 – 13:00	Reversible Programming Models and Paradigms <i>Prof. Kalyan S. Perumalla, Oak Ridge National Laboratory, USA</i>
13:00 – 13:45	LUNCH BREAK
13:45 – 15:30	Reversible Logic Synthesis and RevKit <i>Dr. Mathias Soeken, École Polytechnique Fédérale de Lausanne (EPFL), Switzerland</i>
15:30 – 16:00	TEA / COFFEE BREAK
16:00 – 16:45	Reversible Circuit Testing <i>Prof. Debesh Das, Jadavpur University, Kolkata, India</i>
16:45 – 17:45	Applications of Reversible Circuits and Directions for Future Work <i>Prof. Robert Wille, Johannes Kepler University Linz, Austria</i>